ECEn 487 Digital Signal Processing Laboratory

Lab 5
DIY Signal Processing

Due Dates

This is a two week lab. All TA check off must be completed by Monday, April 14, at 2 PM or the lab will be marked late.

Lab write-up submission, beginning of class Monday, April 14.

On Friday, April 11, we will hold a demonstration period at the end of class, starting at 5 PM. Please plan on taking no more than five minutes to explain your project and to show us how it works.

Laboratory 5 TA Check-off Page (to be submitted with laboratory report)

Student Name: _____

_____ Task 1.1 – Demonstrate your working project to the TA.